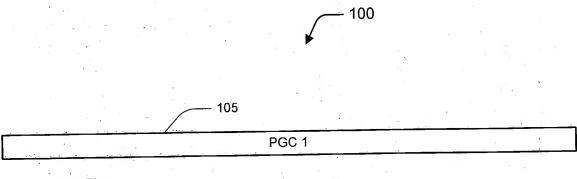
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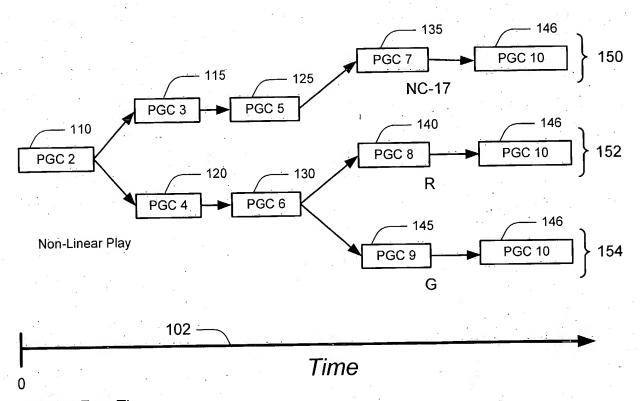
Inventor(s): Hughes

Title: Extended Time-code For

Multimedia







Relative Zero Time

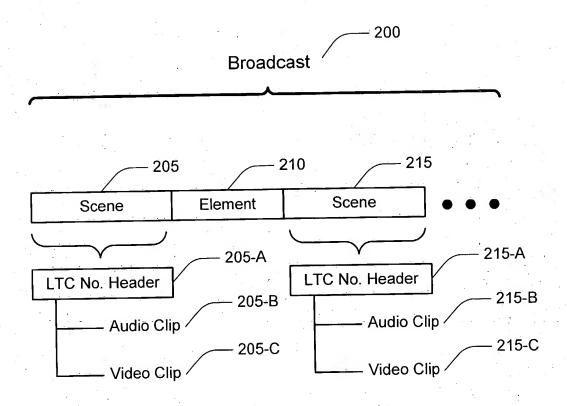
Fig. 1
(Prior Art)

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Inventor(s): Hughes

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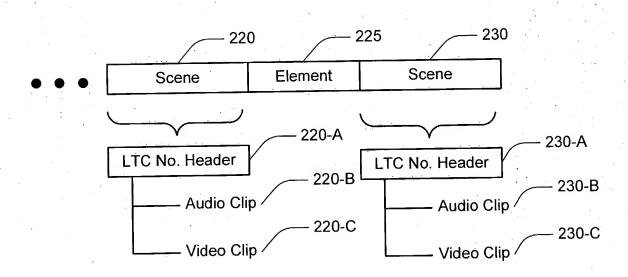


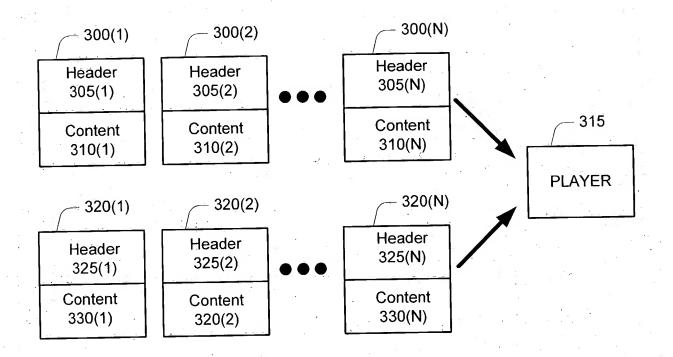
Fig. 2
(Prior Art)

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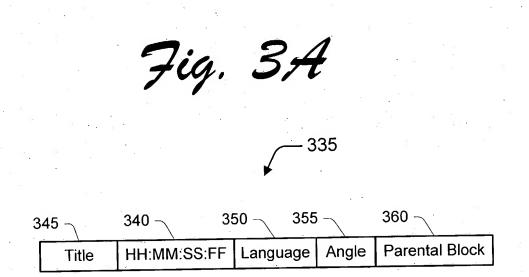
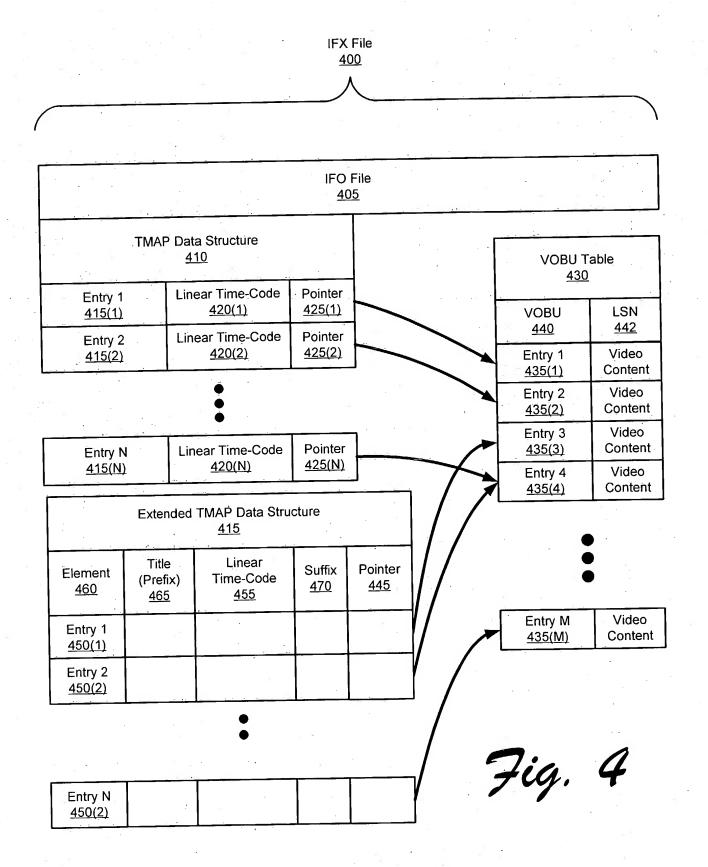


Fig. 38

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Inventor(s): Hughes

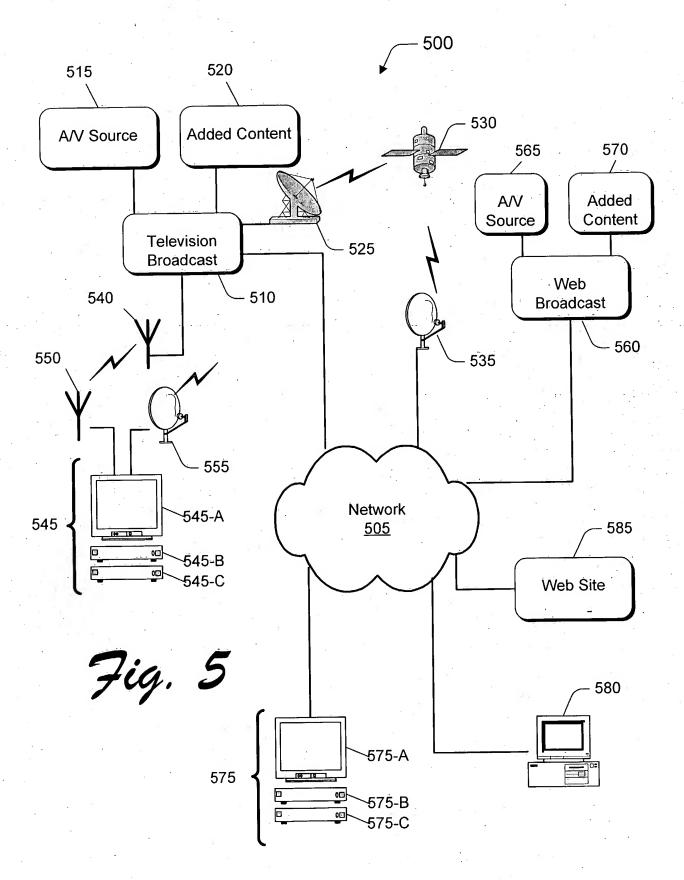
Title: Extended Time-code For



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Inventor(s): Hughes

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Multimedia

600

Receiver

605

Audio / Video Input 610

Processor(s)

<u>615</u>

Multiplexor(s)

<u>625</u>

Time Code Module

620

Multimedia Storage

<u>650</u>

Memory 630

Operating System

<u>635</u>

Multimedia Recorder <u>655</u>

Multimedia Player 660

Application Program(s)

640

Audio / Video Output <u>665</u>

RAM

<u>645</u>

Fig. 6

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Inventor(s): Hughes

Title: Extended Time-code For

Multimedia

Processor(s)

<u>705</u>

Memory

<u>710</u>

Operating System

715

Files/Programs

720

A/V Element Synchronizer

725

Presentation Engine

<u>730</u>

**Navigation Manager** 

<u>735</u>

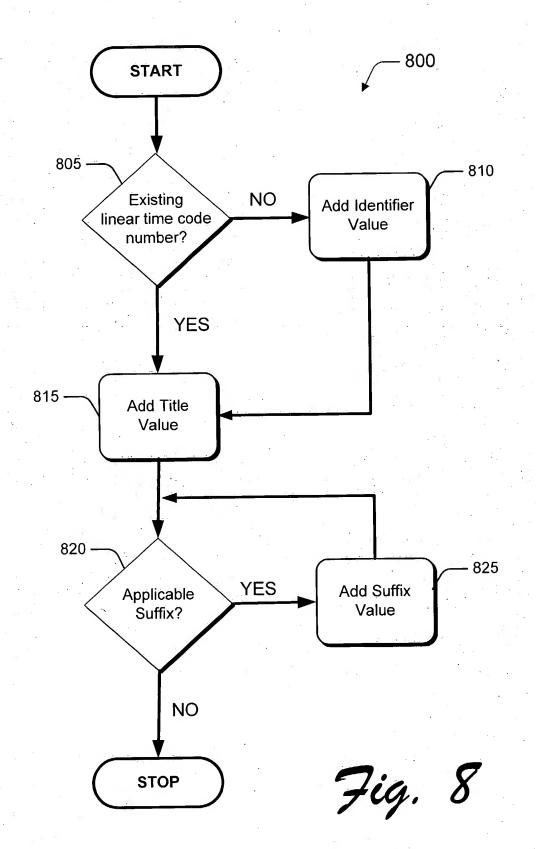
Audio / Video Output

<u>740</u>

Fig. 7

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Title: Extended Time-code For



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Inventor(s): Hughes

Title: Extended Time-code For

